

METHOD AND APPARATUS FOR PROCESSING DATA WITHIN A
PROGRAMMABLE GATE ARRAY USING FIXED AND PROGRAMMABLE
PROCESSORS

ABSTRACT OF THE DISCLOSURE

A method and apparatus for processing data within a programmable gate array begins when a fixed logic processor that is embedded within the programmable gate array detects a custom operation code. The processing continues when the fixed logic processor provides an indication of the custom operational code to the programmable gate array. The processing continues by having at least a portion of the programmable gate array, which is configured as a dedicated processor, performing a fixed logic routine upon receiving the indication from the fixed logic processor.